

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------|------------------|---------|------------------|
| S1 | 724 | 375/355.ccls. | USPAT | OR | OFF | 2007/03/01 08:01 |
| S2 | 0 | 375/355.ccls. and ((clock adj domain) same (programmable adj delay)) | USPAT | OR | OFF | 2007/03/01 08:02 |
| S3 | 1 | 375/355.ccls. and ((clock adj domain) and (programmable adj delay)) | USPAT | OR | OFF | 2007/06/25 09:02 |
| S4 | 0 | 375/354ccls. and ((clock adj domain) and (programmable adj delay)) | USPAT | OR | OFF | 2007/03/01 08:03 |
| S5 | 2 | 375/354.ccls. and ((clock adj domain) and (programmable adj delay)) | USPAT | OR | OFF | 2007/03/01 08:50 |
| S6 | 0 | 375/354.ccls. and ((programmable adj delay) same (select\$6 near add\$5)) | USPAT | OR | OFF | 2007/03/01 08:51 |
| S7 | 1 | 375/354.ccls. and ((programmable adj delay) and (select\$6 near add\$5)) | USPAT | OR | OFF | 2007/06/25 08:39 |
| S8 | 0 | 375/371.ccls. and ((programmable adj delay) and (select\$6 near add\$5)) | USPAT | OR | OFF | 2007/03/01 08:52 |
| S9 | 27 | 375/371.ccls. and ((programmable adj delay)) | USPAT | OR | OFF | 2007/03/01 11:18 |
| S10 | 0 | 375/371.ccls. and ((programmable adj delay)) and (select\$5 near (add\$6 adj delay)) | USPAT | OR | OFF | 2007/03/01 08:56 |
| S11 | 0 | "375"/\$.ccls. and ((programmable adj delay)) and (select\$5 near (add\$6 adj delay)) | USPAT | OR | OFF | 2007/03/01 08:56 |
| S12 | 0 | 375/371.ccls. and ((programmable adj delay) and (clock adj domain)) | USPAT | OR | OFF | 2007/03/01 08:58 |
| S13 | 7 | "375"/\$.ccls. and ((programmable adj delay) and (clock adj domain)) | USPAT | OR | OFF | 2007/03/01 08:58 |
| S14 | 19 | ("4007441" "4419739" "4866310" "5045725" "5224129" "5434996" "5448715" "5689689" "5768529" "5909563" "5961649" "5999023" "6049887" "6081904" "6275547" "6424688" "6577693" "6581164" "6681272").PN. | US-PGPUB; USPAT; USOCR | OR | OFF | 2007/03/01 09:01 |
| S15 | 20 | 375/354.ccls. and ((programmable adj delay)) | USPAT | OR | OFF | 2007/03/01 09:21 |
| S16 | 1 | "6748039".pn. | USPAT | OR | OFF | 2007/03/01 09:21 |
| S17 | 5 | 375/371.ccls. and ((programmable adj delay) and (parallel adj data)) | USPAT | OR | OFF | 2007/03/01 11:37 |
| S18 | 0 | "375"/\$.ccls. and ((programmable adj delay) and (parallel adj data) and (clock adj domain)) | USPAT | OR | OFF | 2007/03/01 11:38 |

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|-----|------|----------------------------------------------------------------------------------------|-------|----|-----|------------------|
| S19 | 5 | ((programmable adj delay) and (parallel adj data) and (clock adj domain)) | USPAT | OR | OFF | 2007/03/01 11:42 |
| S20 | 65 | ((programmable adj delay) and (clock adj domain)) | USPAT | OR | OFF | 2007/03/01 14:20 |
| S21 | 1 | Jazio adj transmitter | USPAT | OR | OFF | 2007/03/01 14:21 |
| S22 | 2512 | ((clock adj domain) and (bi adj directional) (programmable adj delay)) | USPAT | OR | OFF | 2007/03/01 16:33 |
| S23 | 2278 | ((first adj clock adj domain) and (bi adj directional) (programmable adj delay)) | USPAT | OR | OFF | 2007/03/01 16:34 |
| S24 | 14 | ((clock adj domain) and (bi adj directional) and (programmable adj delay)) | USPAT | OR | OFF | 2007/03/01 16:44 |
| S25 | 14 | ((bi adj directional) same (programmable adj delay)) | USPAT | OR | OFF | 2007/03/01 16:47 |
| S26 | 0 | "327"/\$.ccls. and driver and (programmable adj delay) and receiver | USPAT | OR | OFF | 2007/03/01 16:47 |
| S27 | 34 | "327"/\$.ccls. and driver and (programmable adj delay) and receiver | USPAT | OR | OFF | 2007/03/01 17:31 |
| S28 | 0 | "327"/\$.ccls. and (bi adj directional adj asynchro\$7 adj interface) | USPAT | OR | OFF | 2007/03/01 17:31 |
| S29 | 17 | "327"/\$.ccls. and ((bi adj directional) near2 interface) | USPAT | OR | OFF | 2007/03/01 17:33 |
| S30 | 0 | "327"/\$.ccls. and ((bi adj directional) near2 interface) and (programmable adj delay) | USPAT | OR | OFF | 2007/03/01 17:33 |
| S31 | 0 | "375"/\$.ccls. and ((bi adj directional) near2 interface) and (programmable adj delay) | USPAT | OR | OFF | 2007/03/01 17:33 |
| S32 | 0 | "370"/\$.ccls. and ((bi adj directional) near2 interface) and (programmable adj delay) | USPAT | OR | OFF | 2007/03/01 17:33 |
| S33 | 2 | GPS and ((spread adj code) same (synchronization adj tim\$5)) | USPAT | OR | OFF | 2007/03/02 10:39 |
| S34 | 1 | "7123660".pn. | USPAT | OR | OFF | 2007/03/02 11:40 |
| S35 | 1 | "5721923".pn. | USPAT | OR | OFF | 2007/03/02 11:41 |
| S36 | 1 | "5721953".pn. | USPAT | OR | OFF | 2007/03/02 11:41 |
| S37 | 1 | "4839534".pn. | USPAT | OR | OFF | 2007/03/02 11:41 |
| S38 | 6 | 375/354.ccls. and (asynchronous adj interfac\$5) | USPAT | OR | OFF | 2007/06/20 09:55 |

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| S39 | 2 | "375"/\$.ccls. and ((asynchronous adj interfac\$5) same (first adj clock adj domain)) | USPAT | OR | OFF | 2007/06/20 09:56 |
| S40 | 5 | ((asynchronous adj interfac\$5) same (first adj clock adj domain)) | USPAT | OR | OFF | 2007/06/20 10:03 |
| S41 | 0 | 10/605405 | USPAT | OR | OFF | 2007/06/20 09:58 |
| S42 | 5 | ((asynchronous adj interfac\$5) same (first adj clock adj domain)) | USPAT | OR | OFF | 2007/06/20 09:59 |
| S43 | 0 | 10/605405 | USPAT | OR | OFF | 2007/06/20 10:00 |
| S44 | 1 | 10/605405 | US-PGPUB; USPAT; IBM_TDB | OR | OFF | 2007/06/20 10:00 |
| S45 | 224 | (first adj clock adj domain) same (second adj clock adj domain) | USPAT | OR | OFF | 2007/06/20 10:03 |
| S46 | 0 | (first adj clock adj domain) same (second adj clock adj domain) same (first adj receiver) | USPAT | OR | OFF | 2007/06/20 10:04 |
| S47 | 11 | (first adj clock adj domain) same (second adj clock adj domain) same (receiver) | USPAT | OR | OFF | 2007/06/20 10:05 |
| S48 | 4 | (first adj clock adj domain) same (second adj clock adj domain) same (receiver) same delay | USPAT | OR | OFF | 2007/06/20 10:08 |
| S49 | 0 | (first adj clock adj domain) same (second adj clock adj domain) same (receiver) same (delay adj circuit) | USPAT | OR | OFF | 2007/06/20 10:09 |
| S50 | 0 | 10/605405 | USPAT | OR | OFF | 2007/06/25 08:39 |
| S51 | 0 | "10605405" | US-PGPUB; USPAT; IBM_TDB | OR | OFF | 2007/06/25 08:39 |
| S52 | 1 | "10/605405" | US-PGPUB; USPAT; IBM_TDB | OR | OFF | 2007/06/25 08:39 |
| S53 | 2 | 375/355.ccls. and ((clock adj domain) and (programmable adj delay)) | US-PGPUB; USPAT; IBM_TDB | OR | OFF | 2007/06/25 08:39 |
| S54 | 1 | 10/605405 | US-PGPUB; USPAT; IBM_TDB | OR | OFF | 2007/06/25 08:40 |
| S55 | 2 | 375/355.ccls. and ((clock adj domain) and (programmable adj delay)) | US-PGPUB; USPAT | OR | OFF | 2007/06/25 09:02 |
| S56 | 0 | 375/355.ccls. and (first adj clock adj domain) and (asynchron\$ near interfac\$5) | US-PGPUB; USPAT | OR | OFF | 2007/06/25 09:08 |
| S57 | 11 | 375/355.ccls. and (first adj clock adj domain) | US-PGPUB; USPAT | OR | OFF | 2007/06/25 09:39 |

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| S58 | 32 | 375/354.ccls. and (first adj clock adj domain) and delay | US-PGPUB; USPAT | OR | OFF | 2007/06/25 10:21 |
| S59 | 9 | "375"/\$.ccls. and (first adj clock adj domain) and (second adj clock adj domain) and (delay adj circuit) | US-PGPUB; USPAT | OR | OFF | 2007/06/25 10:22 |
| S60 | 27 | 375/354.ccls. and (first adj clock adj domain) and (second adj clock adj domain) | USPAT | OR | OFF | 2007/08/10 16:56 |
| S61 | 22 | 375/354.ccls. and (first adj clock adj domain) and (second adj clock adj domain) and delay | USPAT | OR | OFF | 2007/08/10 17:15 |
| S62 | 0 | 375/354.ccls. and (first adj clock adj domain) and (second adj clock adj domain) and (plurality adj signal adj lines) | USPAT | OR | OFF | 2007/08/10 17:15 |
| S63 | 1 | "375"/\$.ccls. and (first adj clock adj domain) and (second adj clock adj domain) and (plurality adj signal adj lines) | USPAT | OR | OFF | 2007/08/10 17:20 |
| S64 | 1 | "370"/\$.ccls. and (first adj clock adj domain) and (second adj clock adj domain) and (plurality adj signal adj lines) | USPAT | OR | OFF | 2007/08/10 17:25 |
| S65 | 1 | (first adj clock adj domain) and (second adj clock adj domain) and (plurality adj signal adj lines) and delay | USPAT | OR | OFF | 2007/08/10 17:21 |
| S66 | 1 | (first adj clock adj domain) and (second adj clock adj domain) and (plurality adj signal adj lines) and delay\$4 | USPAT | OR | OFF | 2007/08/10 17:21 |
| S67 | 0 | (first adj clock adj domain) and (second adj clock adj domain) and (plurality adj signal adj lines) and (programmable adj delay\$5) | USPAT | OR | OFF | 2007/08/10 17:21 |
| S68 | 3 | (first adj clock adj domain) and (second adj clock adj domain) and (plurality adj signal adj lines) | USPAT | OR | OFF | 2007/08/10 17:28 |
| S69 | 16 | (first adj clock adj domain) and (second adj clock adj domain) and (bi adj (directional)) | USPAT | OR | OFF | 2007/08/10 17:37 |
| S70 | 0 | (first adj clock adj domain) and (second adj clock adj domain) and (asynchronous adj buffer) | USPAT | OR | OFF | 2007/08/10 17:38 |
| S71 | 0 | (first adj clock adj domain) and (second adj clock adj domain) and (asynchronous adj interface) | USPAT | OR | OFF | 2007/08/10 17:38 |